**EE-222 Microprocessor Systems**

**Assignment - I, II and III**

**Group Details:**

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| **Student Name** | **Reg. ID** | **Contribution** |
| M. Saad Jawad | **372985** | Instruction Set Architecture & Control Logic including Memory |
| M. Abubakar Dodhy | **384274** | Registers, Program Counter and Circuit Testing |
| M. Talha | **370860** | Arithmetic Operations and Flags |
| M. Taqi | **372071** | JMP, IJMP and R0 Hardware & GitHub setup |
| Ali Hassan | **391910** | Control Logic including MUX and Decoder |